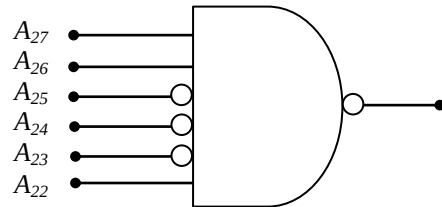


## CSCI 2150 – Memory Interface Homework

Name: \_\_\_\_\_

1. What are the high and low addresses of the memory ranges defined by the chip select shown below?



2. What is the size of the memory chip used with the chip select from problem number 1?
3. What is the size of the processor's memory space for the chip select from problem number 1?
4. Using logic gates, design an active low chip select for a memory device with an address range from  $580000_{16}$  to  $5BFFFF_{16}$  in a 16Meg processor memory space.