

RISC Processors

- Limited and simple instruction set
- Limited and simple addressing modes -- base + offset
- The goal is to bring stuff into the registers and process it, operations are register to register with only simple LOAD and STORE interacting with memory
- Generally, there are many registers to hold all of this data
- Stack pointer is just a general purpose register
- Optimization of pipeline due to better instruction design
- A pipeline is never flushed!